

FIG. 4A

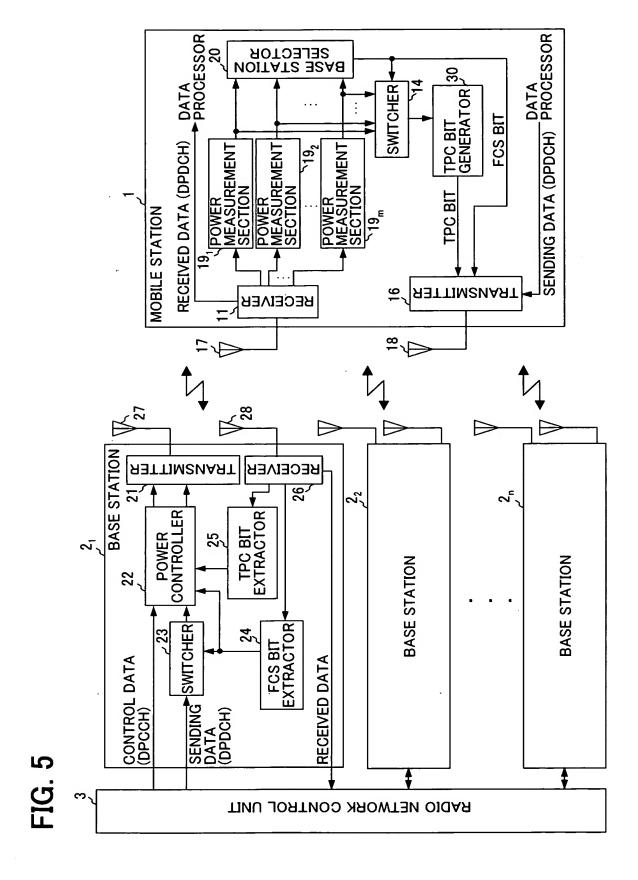
BASE STATION	TPC BIT	DPDCH POWER CONTROL	DPCCH POWER CONTROL
	-	+1dB	+1dB
PRIMARTOELL	0	— 1 dB	— 1 dB
NON-	L	OFF	4 1 dB
PRIMARYCELL	0	OFF	8PO

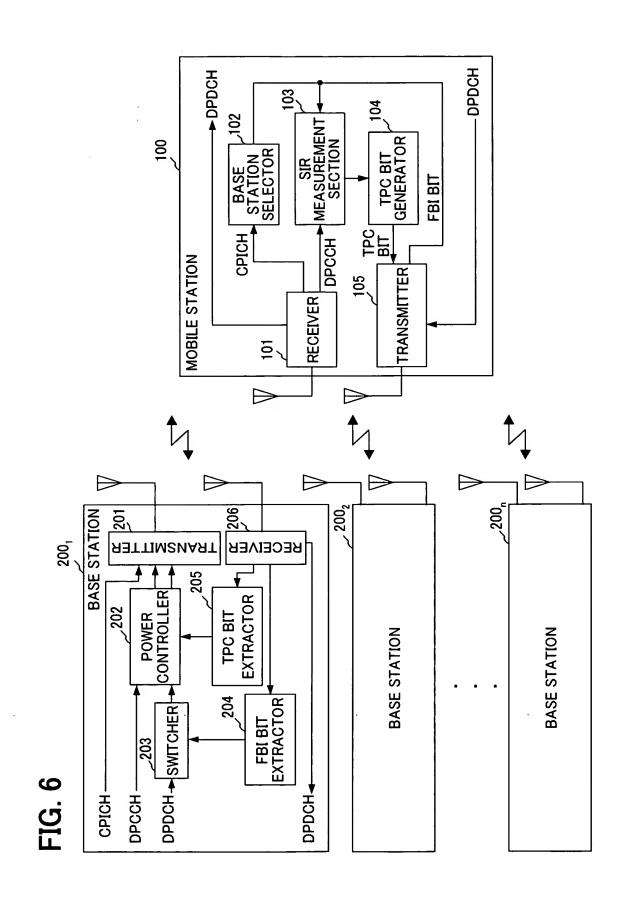
## FIG. 4B

BASE STATION	TPC BIT	DPDCH POWER CONTROL	POWER CONTROL POWER CONTROL
	l	+1dB	+1dB
PRIMARTOELL	0	— 1 dB	— 1 dB
-NON	L	OFF	#0.5dB
PRIMARYCELL	0	OFF	—0.5dB

## FIG. 4C

WER CONTROL	+1dB	—1dB	OdB	9PO
DPDCH   POWER CONTROL   POWER CONTROL	+1dB	— 1 dB	OFF	OFF
тРС ВІТ	l	0	L	0
BASE STATION	I I I I I I I I I I I I I I I I I I I	PRIMARTOELL	-NON	PRIMARYCELL





BASE STATION	TPC BIT	DPDCH POWER CONTROL	DPCCH POWER CONTROL
	L	41dB	<b>9</b> ₽1+
PRIMARTOELL	0	HP1 —	ap1−
NON-	L	<b>340</b>	8P1+
PRIMARYCELL	0	<b>J</b> 40	apı —